



#16/Amndt C  
(NES)  
Q7001

SECOND AMENDMENT UNDER 37 C.F.R. § 1.116  
EXPEDITED PROCEDURE  
GROUP 2816  
PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Docket No: Q65962

Katsuji KIMURA

Appln. No.: 09/940,472

Group Art Unit: 2816

Confirmation No.: 4891

Examiner: Minh T. NGUYEN

Filed: August 29, 2001

For: LINEAR VOLTAGE SUBTRACTOR/ADDER CIRCUIT AND MOS DIFFERENTIAL  
AMPLIFIER CIRCUIT THEREFOR

SECOND AMENDMENT UNDER 37 C.F.R. § 1.116

MAIL STOP AF

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Sir:

In further response to the Office Action dated January 27, 2003, and supplementing the  
Amendment Under 37 C.F.R. § 1.116 filed May 16, 2003, please amend the above-identified  
application as follows:

IN THE SPECIFICATION:

Please replace the formula at the top of page 21 with the following as a typographical error  
has been detected:

RECEIVED  
JUN 26 2003  
TC 2800 MAIL ROOM

ENTER FOR

BRIEF ON 7/14/03